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** CONTINUING DATA ***** MLT None

** FOREIGN APPLICATIONS ***** MLT Yes
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Verified and Acknowledged Examiner's Signature	<i>John L. Cook</i>	Initials	14	36	6

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TITLE

Semiconductor device and method of manufacturing the same

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of

FILING FEE FEES: Authority has been given in Paper

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